August 18, 2017

GUIDELINES FOR SETTING UP SCHEMATIC TEST BENCH

1. Copying the test bench

a. In the cell highlight (left-click) the invx1_TB
b. In view highlight (left-click) schematic
c. In the menu Edit… Copy
d. In To (destination folder) change the library to the your library and cell to your <cell name>_TB
e. Apply… ok

OR

a. In the cell right-click the invx1_TB
b. In view highlight (left-click) schematic and copy
d. In To (destination folder) change the library to the your library and cell to your <cell name>_TB
e. ok

2. Editing the test bench

a. Open <cell name_TB> schematic from your library.
b. Blow out (delete) the inverter symbol
c. Add…….instance
d. Click on Browse
e. Open your library
f. Click on the symbol of your <cell name>
g. Place the symbol on the schematic test bench by clicking it once
h. Close the library browser window and then hit escape (Esc Key)

3. For adding sources and load

a. Add…..instance
b. Click on Browse
c. Click on Analoglib
d. Click on the required component
e. Place it on the test bench by clicking it once.
f. Close the library browser window and then hit escape (Esc Key) or cancel the add instance window

Note: Always connect sources and load to the nodes of the symbol through a wire.

4. For changing the input source properties

a. Highlight the source by clicking it
b. Hit shift-q key and edit the properties as required
c. In case more than one source is required, copy the source and edit its properties.

5. For adding text to the test bench

a. Create…..note…….text…..
b. Write the note and place it on test bench by clicking it once on test bench schematic and then hit escape (Esc key)